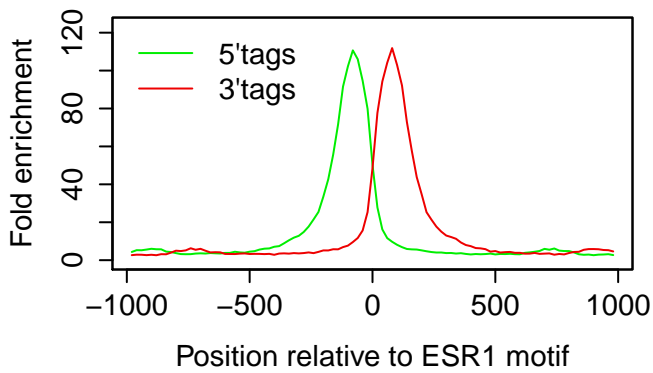
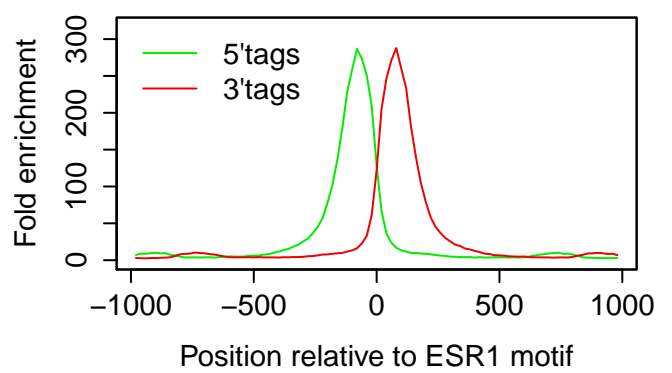


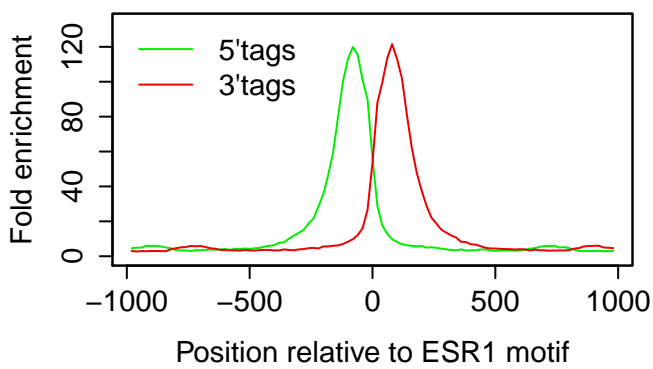
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/Ctl | rep1**



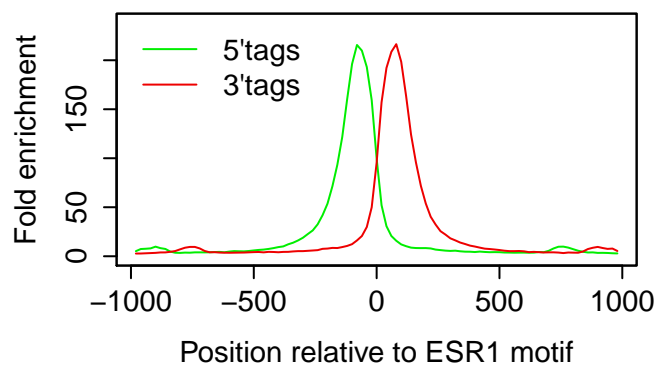
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/Ctl | rep2**



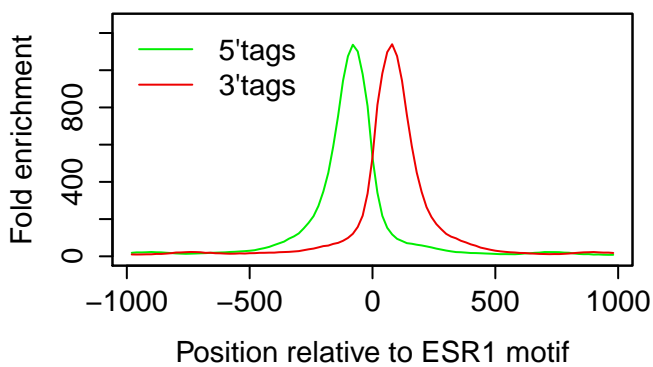
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/Ctl | rep3**



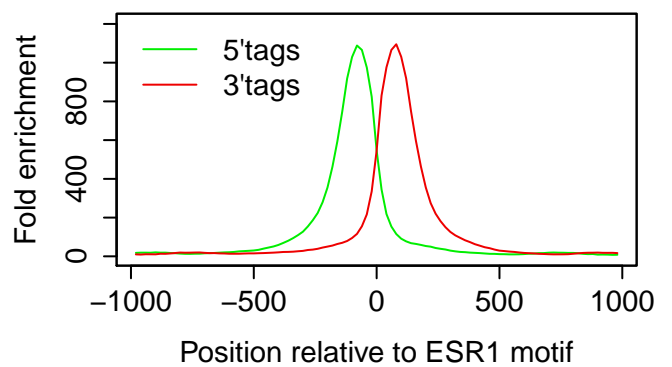
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/Ctl | rep4**



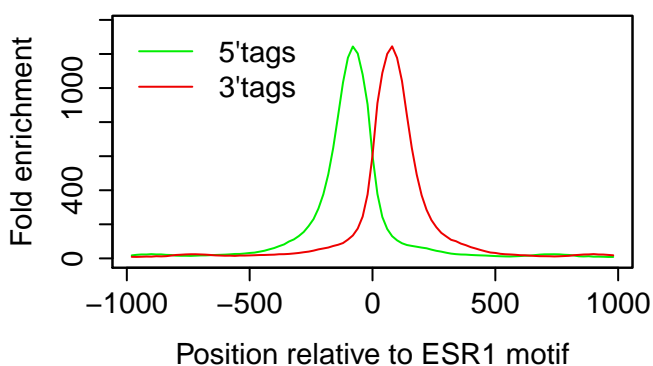
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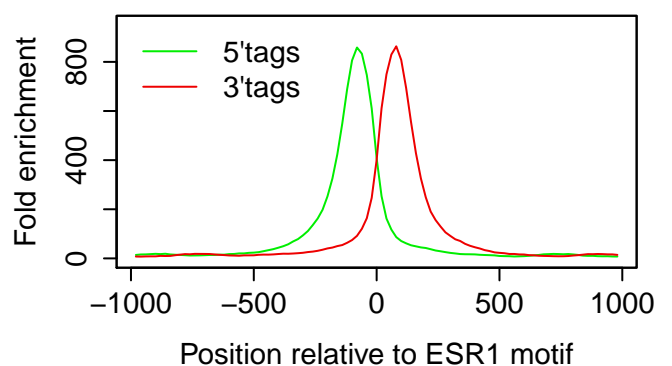
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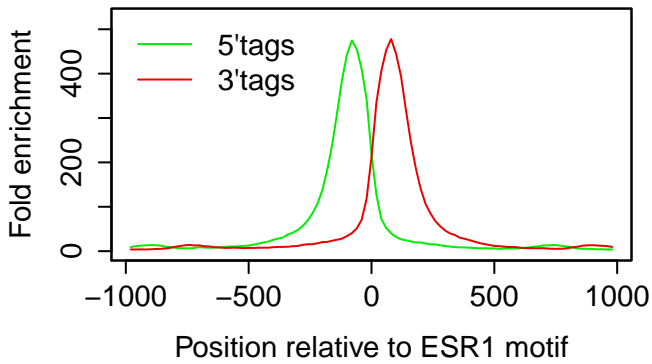
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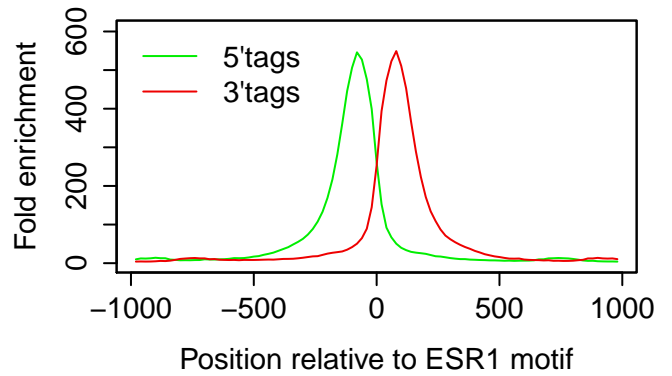
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/E2 | rep4**



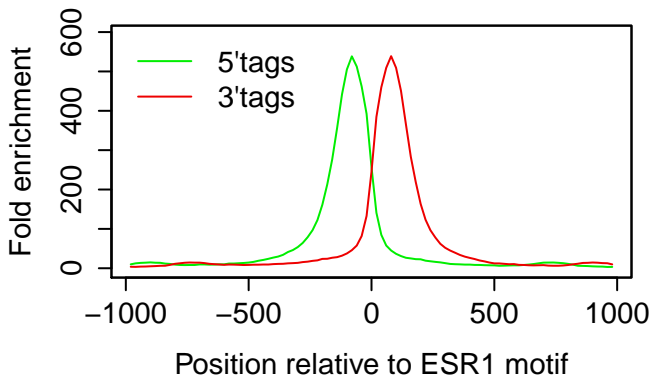
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/FI | rep1**



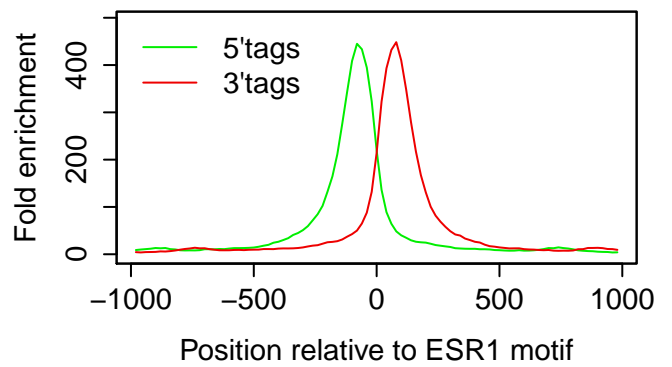
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/FI | rep2**



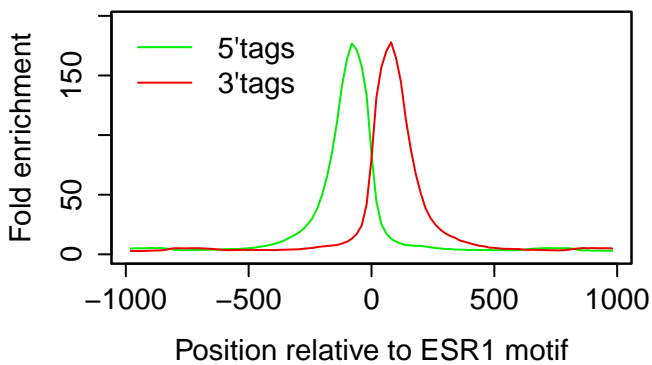
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/FI | rep3**



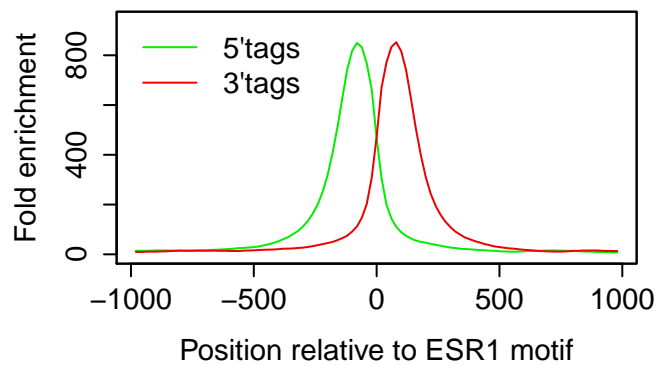
**MCF7-V:trans-ZEB1 | ESR1 | +DOX/FI | rep4**



**MCF7-V:trans-ZEB1 | ESR1 | +DOX/Ctl | all**



**MCF7-V:trans-ZEB1 | ESR1 | +DOX/E2 | all**



**MCF7-V:trans-ZEB1 | ESR1 | +DOX/FI | all**

